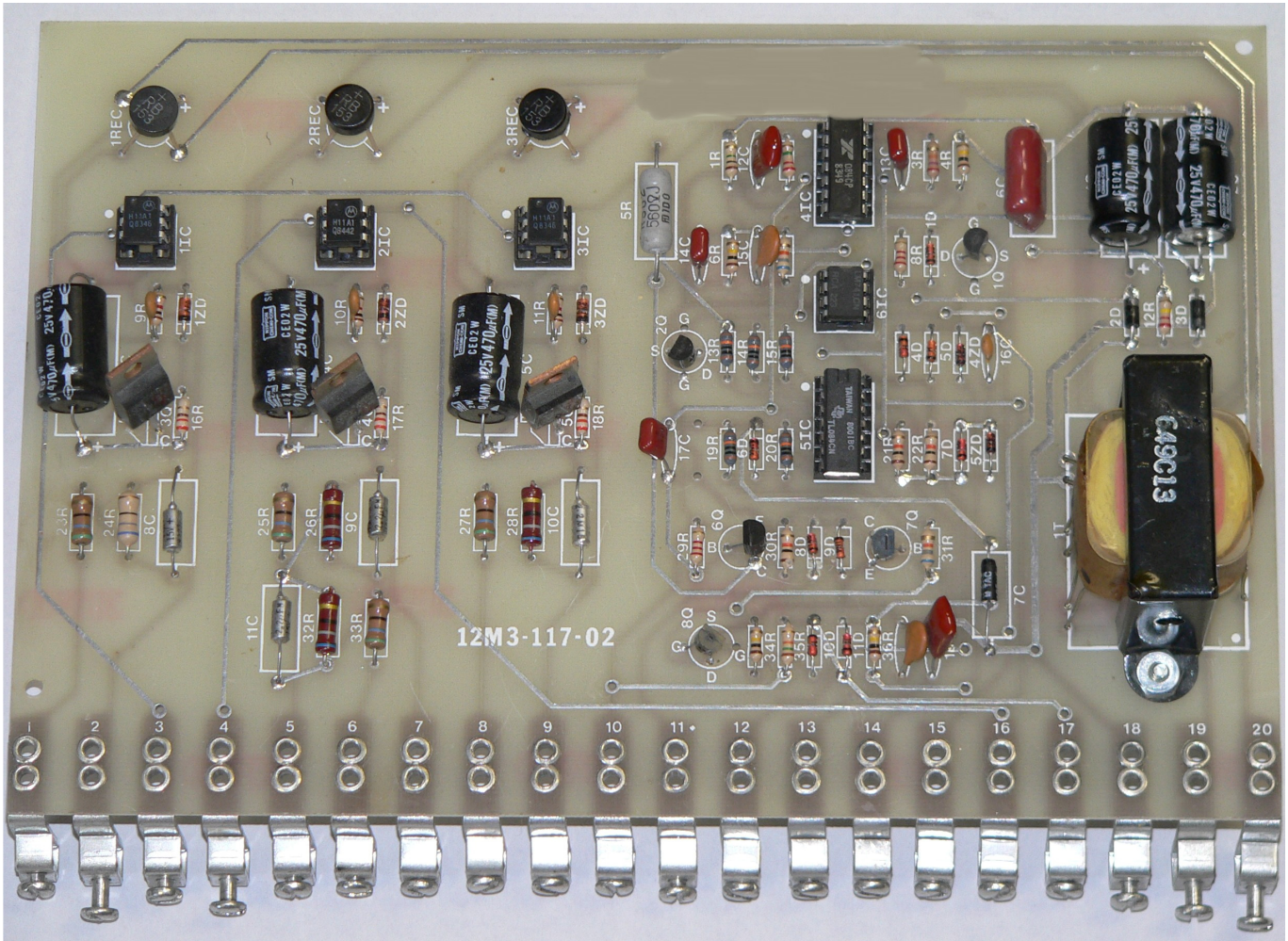




Trouble-Shooting Manual MODEL 201 FIELD FIRING ASSEMBLY PART NUMBER 12M03-00117-02



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GEMINI MODEL 201 FIELD FIRING ASSEMBLY

PART NUMBER 12M03-00117-02
SCHEMATIC DIAGRAM 12M03-00117-02

I. SPECIFICATIONS

SUPPLY

- 120 volts AC \pm 10%
- 50/60 Hz, Single Phase

TEMPERATURE

- 0° to 40°C (32° to 104°F) 50°C in cabinet

INPUTS

A. Firing Control Input:

Operational Amplifier with summing junction and output connected to external terminals for components as required to establish gain and feedback compensation. The amplifier is connected in an inverting configuration and causes firing of a thyristor power circuit when the input has a net negative value.

B. Other Inputs:

- 1) "Enable" input allows circuit to be reset to zero for field economy and during power up.
- 2) "Inhibit" input allows circuit to be totally disabled when the system requires this function.
- 3) "Crossover" input connects to an external crossover circuit for DC motor operation above base speed.

OUTPUTS

A. Firing Signal:

Four Firing Signal Outputs; two isolated and two with a common output for connection to the two legs of a single phase converter with a common cathode.

Magnitude: Initial pulse into a short circuit load of 200 mA, tapering exponentially to 100 mA which terminates at the end of the half cycle.

Range of Control: From continuous signal at maximum output to Phase Control from 0° to a maximum delay of approximately 160° at End Stop.

B. Half Cosine Wave for use as a timing waveform for REFLEX MODEL 220 bi-directional firing.

II. THEORY OF OPERATION

The REFLEX Model 201 Field Firing Assembly is a versatile unit that provides isolated gate drive for a variety of single phase thyristor power converter configurations to supply DC excitation to highly inductive fields such as those in DC Motors and Generators, AC Alternators, and Eddy Current Clutches and Brakes. The field is discharged regeneratively when reducing current to improve response.

When used with the REFLEX Model 220 Bi-directional Output Adaptor it provides bi-directional field current for applications such as reversing field control of a DC Generator, a Field Reversing Regenerative DC Motor Drive, or for alternate excitation of two fields such as an Eddy Current Clutch and Brake.

When used with the Model 223 Crossover Assembly, the speed range of a DC Motor is extended by field weakening in either regenerative or non-regenerative drives.

The Model 201 Field Firing Assembly consists of the following elements as shown in the Simplified Schematic Diagram (Figure 1):

1. Power Supply
2. Half-cosine Generator
3. Summing Amplifier
4. Comparator
5. Gate Drive

1. Power Supply – The power supply uses a transformer with four identical 12 volt secondary windings.

One 12 volt winding in conjunction with diodes 2D and 3D and two 470MF filter capacitors provides a nominal plus and minus 15 volts DC for the Half-cosine Generator, Summing Amplifier and Comparator.

Each of the other three 12 volt windings with a bridge rectifier and 470MF filter capacitor provides an electrically isolated power supply for the Gate Drive.

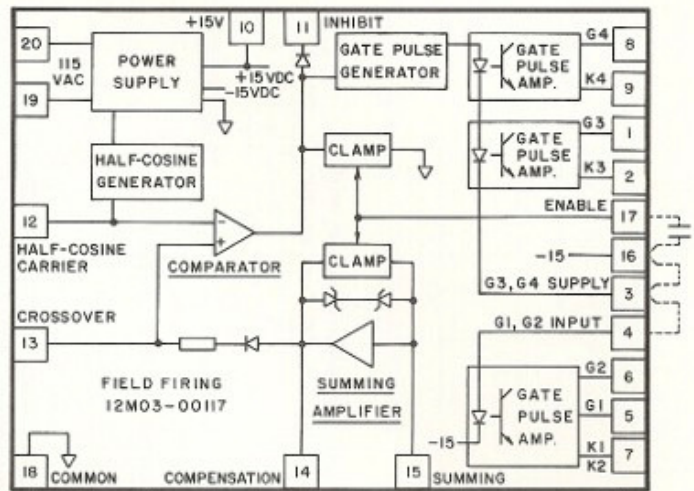


FIGURE 1. SIMPLIFIED SCHEMATIC DIAGRAM

2. **Half-Cosine Generator** – If the relationship between firing angle and DC output of a single phase controlled rectifier bridge is analyzed, it is found that:

$$V_{dc} = .45 E_{ac} (1 + \cos a) \quad \text{EQ. 1}$$

Where “ V_{dc} ” is the average DC value for a given half cycle of the AC wave, “ E_{ac} ” is the RMS value of the AC waveform, and “ a ” is the firing angle. Therefore, when a cosine relationship between input firing signal and firing angle is established, the DC output will be a linear function of the input signal. Linearity allows accurate open loop control of output voltage, simplifies closed loop analysis and improves performance.

The integrator 4I(A) receives a nominal 12 volts AC from the power supply transformer and using a 220K ohm resistor and .047MF capacitor for scaling, produces a nominal 2.5 volt cosine wave for 60 Hz and 3.0 volts for 50 Hz.

Because of the full-wave nature of a power bridge, a half-cosine wave is needed for each of the half cycles of the AC line. This requirement is accomplished by 5I(A), an op-amp with a gain of one that can be switched between non-inverting and inverting by field effect switching transistor, 2Q. The switching is performed at the zero crossing of the AC line.

To maintain purity of the half-cosines, the switching from inverting to non-inverting must not be affected by line noise spikes. For this reason, op-amp 4I(D) performs a second integration on the cosine wave to produce a –sine wave, almost perfectly pure because of the double integration. 4I(C) acts as a comparator to determine the zero crossings and to operate the FET switch accordingly.

To add the “1” to the cosine wave to obtain the (1+cos) function, a peak detector, 5I(B) determines the positive peak of the half-cosine waves, and adds it to the waves. The DC “1” is stored in a 1MF capacitor, which is allowed to discharge slowly to allow the circuit to follow line voltage or frequency variations. The final resulting waveform is connected to one input of the Comparator 5I(C).

To establish the firing angle, “ a ” in Equation 1, a variable positive DC voltage is connected to the other input of the Comparator, and a firing signal is produced at the time when the “1+cos” voltage becomes more negative than the DC voltage.

The “1+cos” signal is shifted slightly negative with respect to zero to provide an “End Stop,” pulse for regenerative discharge of the field current. The “1+cos” signal is also brought out to terminal 12 as a “Half-cosine Carrier” for use with the Model 220 Bi-directional Adaptor.

3. **Summing Amplifier** – Input amplifier 6I provides the means for summing reference and feedback signals. Its input, pin 2, is brought out to terminal 15 and its output, pin 6, is brought out to terminal 14 to allow flexibility in input scaling, and selection of various feedback networks to suit a particular application.

With the input and output resistors connected for unity gain, a negative signal from 0 to 6 volts will provide a control range from 0 to full output. More commonly, a positive field current feedback signal of 2 volts at maximum field current is applied to the input, terminal 15, through a 2.2K resistor to provide a nominal 1mA feedback current to the summing junction at pin 2 of 6I. A negative reference current slightly in excess of the feedback current applied to the same summing junction will produce full current to the field. Zener diodes 4ZD and 5ZD act as a clamp to limit output voltage of the Summing Amplifier to plus or minus 6 volts.

To operate the assembly, a negative 15 volts from any source with respect to circuit common must be applied to “Enable” terminal 17. If the “Enable” terminal is open circuited, field effect switching transistor, 1Q, conducts, clamping the output of the Summing Amplifier to zero.

4. **Comparator** – The output of the Summing Amplifier is compared to the (1+cos) wave by op-amp 5I(C), acting as a voltage comparator. Its output drives a pair of transistors, 7Q and 6Q, to produce gate signals to the thyristors in the power converter.

If the output of the Summing Amplifier is negative, no gate signals appear. As it moves positive, gate signals occur earlier and earlier in the half-cycle until full conduction occurs when the output of the Summing Amplifier is more positive than the positive peaks of the (1+cos) wave.

One input, pin 10, of the Comparator is brought out to terminal 13 for use with the Model 223 Crossover Assembly.

The input of transistor 7Q is short circuited by FET switch 8Q at the same time the Summing Amplifier is clamped to avoid indiscriminate firing on power up. It can also be disabled by connecting the “Inhibit,” terminal 11, through a 10K resistor to minus 15 volts.

5. **Gate Drive** – Transistors 7Q and 6Q provide signals to the diode section of two or three opto-couplers 1I, 2I, and 3I in series. A jumper option is provided to allow driving opto-coupler 2I from a separate source.

Fast response is achieved by reversing the field voltage for regenerative discharge of the field as long as the field is conducting to provide fast field discharge. The half-cosine wave is biased to generate an “End Stop” pulse in the absence of an input signal near the end of the AC supply cycle.

If the field is conducting near the end of a negative half cycle, the “End Stop” pulse prevents conduction in the next positive half cycle by commutating the field current to a thyristor pair that conducts in the next negative half cycle.

The isolated output of each of the three opto-couplers drives a Darlington power transistor which provides the gate drive to the thyristors. Transistor 4Q provides output to two terminals for individual gate drive to each of two thyristors with a common cathode connection.

A high level of noise immunity is achieved by biasing the output transistors so that a significant output current from the opto-coupler is required before an output pulse is generated. Unwanted signals, which may be caused by high dv/dt acting on the input-output capacitance of the opto-coupler are effectively by-passed without triggering an output.

Although the opto-coupler itself has an unacceptably slow rise time, only a small portion of the rising waveform is needed for full output, and the output pulse has a rise time of less than 0.5 microseconds. Output pulses, therefore, qualify as "hard firing."

VOLTAGE CHECK

1. The primary voltage of transformer 1T, leads 1 and 2 (terminals 20 and 19), should be 115V AC.
2. The secondary voltage of 1T should be 120V AC nominal in each of four windings:
 - leads 9 & 10 (terminal 18 to the anode of 2D)
 - leads 8 & 7 (AC input to 3 REC)
 - leads 5 & 6 (AC input to 2 REC)
 - leads 3 & 4 (AC input to 1 REC)
3. Measure +12V DC nominal between terminal 18 (common) and terminal 10.
4. Measure -12V DC nominal between terminal 18 (common) and terminal 16.
5. Measure +15V DC nominal between terminal 7 (common) and the positive end of capacitor 4C.
6. Measure +15V DC nominal between terminal 2 (common) and the positive end of capacitor 3C.
7. Measure +15V DC nominal between terminal 9 (common) and the positive end of capacitor 5C.
8. Use an oscilloscope to verify that waveforms are as shown on the schematic diagram.

COMPONENT LIST - ASSEMBLY #12MO3-00117-02

Symbol	Part #	Description (Acceptable Substitute)*	Symbol	Part #	Description (Acceptable Substitute)*
1T	04P01-00004	Transformer-120V AC PRI, four 10V SEC @ 50mA (Coil Trans.-120-4-10)	13,14,17C	03P07-10410-00	Capacitor-0.1MF, 100V, Film
1-3REC	05P01-00003	Rectifier Bridge-50V, 1A (EDI-PF50)	15, 18C	03P06-10205-00	Capacitor-0.001MF, 50V, Ceramic
1,4-11D	05P02-00001	Diode-Signal, 50mA, 200 PIV (1N4148)	16,20-22C	03P06-10305-00	Capacitor-0.01MF, 50V, Ceramic
2,3D	05P01-00001	Diode-Medium Power, 1A, 400 PIV (1N4004)	1R	01P01-10500-02	Resistor-1.0M, 1/4W, 5%
1-3ZD	05P03-00002	Zener Diode-4.3V, 500mW, 5% (1N5229B)	2R	01P01-22500-02	Resistor-2.2M, 1/4W, 5%
4,5ZD	05P03-00005	Zener Diode-6.8V, 500mW, 10% (1N5235B)	3R	01P01-27300-02	Resistor-27K, 1/4W, 5%
1,2,8Q	05P05-00001	Transistor-N Channel JFET (2N4093)	4, 6, 34, 36R	01P01-10400-02	Resistor-100K, 1/4W, 5%
3-5Q	05P04-00009	Transistor-Darlington (GE-D41K1)	5R	01P01-56103-02	Resistor-560, 2W, 5%
6Q	05P04-00001	Transistor-PNP, Small Signal (2N3638A)	7,31R	01P01-68300-02	Resistor-68K, 1/4W, 5%
7Q	05P04-00002	Transistor-NPN, Small Signal (2N3392)	8,29R	01P01-22200-02	Resistor-2.2K, 1/4W, 5%
1,2,3IC	05P10-00001	Opto-Isolator (GE-H11A1)	9-11R	01P01-10200-02	Resistor-1.0K, 1/4W, 5%
4-5IC	05P08-00002	Quad Op-Amp (TI-TL084)	12R	01P01-22400-02	Resistor-220K, 1/4W, 5%
6IC	05P08-00011	Op-Amp (LM741)	13-15, 19, 20R	01P02-10031-01	Resistor-100K, 1/2W, 1%
1-5C	03P01-47102-01	Capacitor-470MF, 25V, Electrolytic	16-18R	01P01-33100-02	Resistor-330, 1/4W, 5%
6C	03P07-10510-00	Capacitor-1.0MF, 100V, Film	21,22,30R	01P01-10300-02	Resistor-10K, 1/4W, 5%
7C	03P03-10503-00	Capacitor-1.0MF, 35V, Tantalum	23, 25, 27, 33R	01P01-56000-02	Resistor-56, 1/2W, 10%
8-11C	03P03-22501-00	Capacitor-2.2MF, 15V, Tantalum	24, 26, 28, 32R	01P01-68000-02	Resistor-68, 1/2W, 10%
12,19C	03P07-47310-00	Capacitor-0.047MF, 100V, Film	35R	01P01-15300-02	Resistor-15K, 1/4W, 5%

* OR EQUAL

BENCH TEST

TEST MATERIAL REQUIRED:

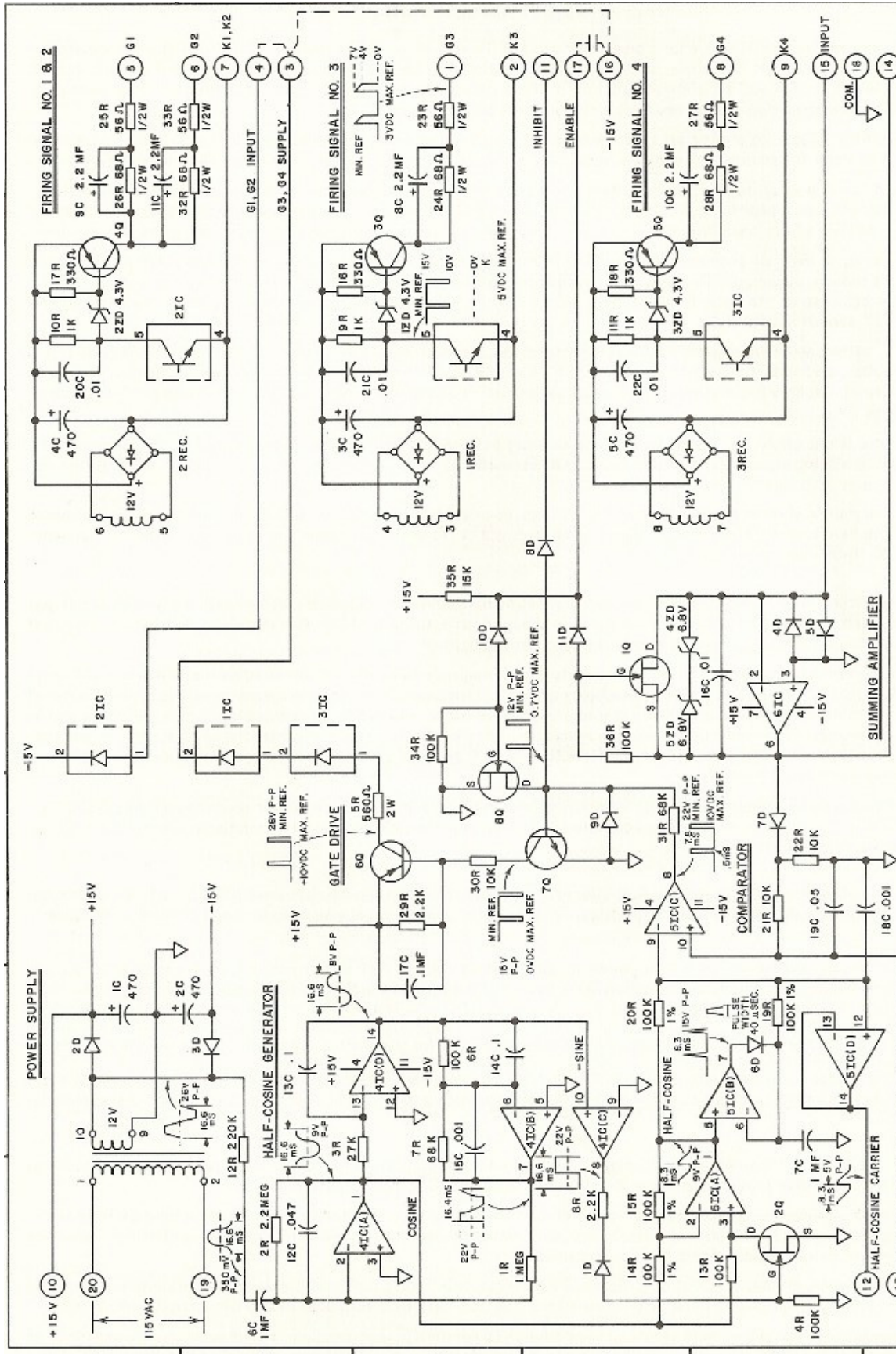
- 1 – 120V AC line cord
- 1 – 5K, 2 watt potentiometer
- 1 – 100K, ¼ watt resistor
- 2 – 10K, ¼ watt resistors
- 4 – 47 ohm, ½ watt resistors
- 11 – Clip leads
- 1 – Oscilloscope (Tektronix 2213 or equal)
- 1 – Digital Voltmeter (Beckman HD-110 or equal)

1. Attach a 5K Potentiometer with the high end (CW) to terminal 16, low end (CCW) to terminal 18, and wiper to one end of a 10K Resistor. Attach the other end of this resistor to terminal 15. Turn the 5K Potentiometer full CCW.
2. Attach a 10K Resistor from terminal 15 to 14.
3. Connect jumpers from terminal 16 to 17 and from 3 to 4.
4. Attach a 47 Ohm ½ Watt Resistor between each of the following pair of terminals: 5 & 7, 6 & 7, 1 & 2, and 8 & 9.
5. Apply 115V AC to terminals 19 & 20.
6. With an oscilloscope, check the waveform at terminals 12 (+) and 18 (-). Set the oscilloscope vertical input to 1V per division, and sweep rate to 2mS/division. Read a ½ cosine pattern of 5V peak, which repeats at 8.3mS intervals. The negative peak of the half cosine wave should be at approximately DC zero.
7. Change the oscilloscope vertical input to 2V per division.
8. Place the oscilloscope across each of the 47 Ohm resistors and read a pulse of approximately 0.4 mS width, with a leading peak of 6 to 8 volts, dropping to 3 to 5 volts, which repeats at 8.3 mS intervals.
9. Turn the 5K potentiometer fully CW and back to zero. The pulse should change width until it forms a DC level and then return to original pulse when potentiometer is returned to 0.
10. Repeat steps 8 and 9 with the other 3 resistors. To keep polarity on the oscilloscope the same, use terminals 7, 2 and 9 as common.
11. Select any one of the resistors and set the 5K potentiometer CW until just reaching DC level. Connect a 100K resistor from terminal 16 to terminal 13. The DC level should drop to a pulse.
12. Connect a 10K Resistor between terminals 16 and 11. All pulses should disappear. Disconnect this resistor.
13. Remove the jumper from terminal 17 and the pulses should disappear.



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REVISIONS		
REV.	DESCRIPTION	DATE
2	ADD CAPACITORS 20C, 21C, 22C	10-63 AW
1	ADD BUFFER 51C(D) CHANGE SUMMING AMP. TO 61C	2-24-62 AW
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